

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re PATENT APPLICATION of:

Robert L. FRANCH et al.

Appln. No.: Not yet assigned

Art Unit:

Filed: Herewith

Examiner: Unknown

For: BUILT IN SELF TEST CIRCUIT FOR
MEASURING TOTAL TIMING
UNCERTAINTY IN A SYNCHRONOUS
DATA PATH

INFORMATION DISCLOSURE STATEMENT

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

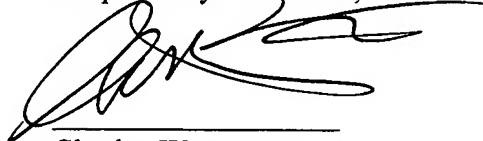
Sir,

Pursuant to the duty of disclosure under 37 C.F.R. §1.56, submitted herewith is an Information Disclosure Statement (IDS) including a Form PTO-1449. Pursuant to a Pre-OG Notice dated July 11, 2003 waiving requirement for copies of U.S. Patents and Published Applications, copies of Patents cited on the PTO-1449 are not included.

Since this IDS is being submitted with the application and prior to a first office action, no certification or fee is believed required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

November 13, 2003
(Date)

Respectfully Submitted,



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Date of Deposit

November 13, 2003

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